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APPLICANTS Takamitsu Yamada, Kanagawa-Ken, JAPAN; Yasutaka Tsukamoto, Kanagawa-Ken, JAPAN; Hidetaka Minami, Kanagawa-Ken, JAPAN; ** CONTINUING DATA <i>none pme</i> ** FOREIGN APPLICATIONS <i>yes pme</i> JAPAN 2003-018920 01/28/2003 IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 04/28/2004					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and Acknowledged <i>pme</i> Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 13	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 6
ADDRESS 24998 DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2101 L Street, NW Washington, DC 20037					
TITLE Semiconductor integrated circuit and scan test method therefor					
FILING FEE RECEIVED 1230	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)		